Abstract of the Disclosure

An inverter apparatus includes an inverter for driving a 3-phase motor, a power supply circuit for applying direct current voltage to the inverter, a switching element mounted above and below an arm of each phase, a smoothing capacitor for protecting a voltage applied to the inverter from reaching a value equal to or larger than a predetermined value, and a control circuit for generating a control signal for control of an ON/OFF switching operation of each switching element. The control signal is generated by the control circuit such that the ON timing of each switching element of each phase can be shifted from each other.